An Elegant Impulser Developed for Flat Beam Injection

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ABSTRACT

The following report describes the design, construction, and checkout of a high-voltage (HV) impulser built for the heavy ion fusion (HIF) project [1]. The purpose of this impulser is to provide an adjustable diode voltage source of sufficient quality and level to allow the optimization of beam transport and accelerator sections of HIF [2, 3]. An elegant, low-impedance, high-energy storage capacitor circuit has been selected for this application. Circuit parameters of the retrofit to the diode region [4] have been included to provide the controlled rise time. The critical part of this circuit that is common to all candidates is the impedance matching component. The following report provides a description of the implemented circuit, the basic circuit variables for wave shaping, screening techniques revealing the weakest circuit component, and the resulting output of the injector.

INTRODUCTION

The purpose of this effort is to provide a regulated impulse to a thermionic diode. The diode provides the ion beam foundation for HIF. The quality of the impulse regulation directly affects the initial beam profile and the required complexity of the acceleration section. Poor beam quality prematurely compromises the level of acceleration to the beam instability level. Table 1 summarizes the design requirements of the injector.

Several approaches used to meet the impulse requirements have shown modest success at limited levels. An elegant low-impedance source was selected as the workhorse for the impulser. Screening major components exposes weak components and shows areas for improvement. The general requirement to control the diode electrical characteristics provided the insight to high beam quality.

Table 1 Initial engineering design requirements used to develop the proper impulse generator for the HIF project.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Units</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Flat Top Voltage (FTV)</td>
<td>kV</td>
<td>75</td>
</tr>
<tr>
<td>pulse width</td>
<td>µs</td>
<td>1</td>
</tr>
<tr>
<td>rise time (10-90%) @ 80 kV</td>
<td>ns (min/max 5 %)</td>
<td>310</td>
</tr>
<tr>
<td>rise time (10-90%) @ 100 kV</td>
<td>ns (min/max 5 %)</td>
<td>277</td>
</tr>
<tr>
<td>fall time (10-90%) @ 80 kV</td>
<td>ns (min/max 5 %)</td>
<td>310</td>
</tr>
<tr>
<td>fall time (10-90%) @ 100 kV</td>
<td>ns (min/max 5 %)</td>
<td>277</td>
</tr>
<tr>
<td>overshoot</td>
<td>% of nominal FTV</td>
<td>1</td>
</tr>
<tr>
<td>ripple</td>
<td>% of nominal FTV</td>
<td>0.1</td>
</tr>
<tr>
<td>Flatness (droop) over 5 µs</td>
<td>% nominal FTV</td>
<td>0.5</td>
</tr>
<tr>
<td>jitter (pulse to pulse)</td>
<td>ns</td>
<td>20</td>
</tr>
<tr>
<td>repetition rate</td>
<td>Hz</td>
<td>1</td>
</tr>
</tbody>
</table>

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Impulser

A system-level look at the diode or load requirements exposed an inherent problem with mismatched impedances. The basic solution to resolve the diode parameter requirement is applicable to any selected impulse technology. The following circuit shown in Figure 1 represents the first order approach to provide a stiff source as the impulser.

The basic circuit consists of a 100 nF/200 kV capacitor (C1), a series UV-illuminated gas switch (S1), and impedance matching resistor (R3), a variable augmenting diode capacitor (C2), a robust crowbar switch (S2), and a crowbar resistor (R2). The interconnections are defined by inductance (L1, L2, and L3). The rise time is adjusted to the desired profile with L3 and C2. The overshoot is controlled by the proper selection of R3. The impedance of the interconnection L3 to the diode and capacitance C2 defines the value of R3.

Figure 1 The above figure illustrates the basic circuit used as the HIF diode impulser.

Critical to the proper operation of this type of circuit is the selection of R3. The parameter that is controlled by this decoupling component is the overshoot. The optimum value for this component is given by the following relationship:

\[ R_3 = 2.3 \times \sqrt{\frac{L_3}{C_2}} \]

In addition:

- \( C_1 = 100 \) nF
- \( C_2 = 10 \) nF
- \( L_1 = 200 \) nH
- \( L_2 = 200 \) nH
- \( L_3 = 1.3 \) \( \mu \)H
- \( R_1 = 100 \) k\( \Omega \)
- \( R_2 = 3 \) \( \Omega \)
- \( R_3 = 26.2 \) \( \Omega \)

S1—Provides for the initiation of the impulse into the load.
S2—Provides for a triggered crowbar before diode closure. The final circuit may incorporate a fault detector to ensure that if diode closure does occur, then the crowbar will activate and discharge the system in approximately 500 ns.

Consider the test configuration with an initial charge voltage of 100 kV, and a selected C2 of 10 nF. The impulser with a selected width of 5 \( \mu \)s will have 500 joules (J) stored in C1 and transfer 50 J into C2. The pulse width is only given as a reference and needed to view any power issues. A diode intrinsic capacitance of 100 pF will store 0.5 J at 100 kV. C2 becomes the main energy storage component in the diode region. The energy stored in C2 discharges through R2 and R3 at a rate defined by L3 and C2. Consider that L3 with a length of one-meter has an inductance of approximately 1.3 \( \mu \)H. The resulting frequency is defined by the following relationship:

\[ F = \frac{1}{2\pi\sqrt{LC}} \]

The resulting frequency is 1.4 MHz with a period of 720 ns. The contributing factor of L3 to the discharging impedance is given by \( \omega L \), which for this case is 14 \( \Omega \). The dynamic impedance of L3 and C2 given by the square root of the ratio of L3 to C2 is 11.4 \( \Omega \). The combined R2 an R3 value is nominally 29 \( \Omega \). The resulting Q of the circuit is slightly greater than 2. The effect of this to the discharge characteristics of the diode will be similar to those of the impulse charging and limit the undershoot during the crowbar phase. The result is a fall time on the diode approximately equal to the rise time.

This approach provided an elegant solution to the impulser requirements in a timely fashion. The selected circuit components provided a robust design that should provide a reasonable project evaluation tool. The next generation of impulsers required beyond the immediate project needs should take these issues into consideration as an integrated approach to an engineered injection diode.

Impulser Layout

The initial intent of the impulser layout was to reduce the overall package of the unit and relax the need for an oil environment. The typical packaging of choice is pressurized gases.
Figure 2 illustrates a compact topology to fit into a reasonable environmental chamber. Shown are the energy storage capacitors, series and crowbar switches, an internal voltage probe on the series switch output, triggering inputs, and the first set of crowbar resistors. A compromise use of oil was required to accommodate the HV-bushing/filament vacuum interface. This insulator is not rated for pressure greater than an atmosphere. Fit-up and checkout of the impulser was done in a 55-gallon barrel providing a reasonable compact topology.

**Figure 2** The above photo illustrates a compact package technique. The design of this package allows operation to 50 kV in one atmosphere of air and higher levels in SF6 or oil environments.

### Major Component Screening

Three major components in the impulser were screened to improve system reliability. These components were the 40-nF/100 kV energy storage capacitor, the 3-nF/40 kV pulse shaping capacitors, and the 100 kV crowbar gas switch. The following section describes the screening process and related information to the weakest component the principal energy storage capacitor.

Ten 40-nF/100 kV capacitors provide the primary energy stage for this impulser. The 120 kV upper level of desired operation provided a design safety margin of 2 for the main storage section C1, as previously defined. The initial configuration required that the capacitor be qualified for a gas environment. The units were cleaned of surface contaminates then screened in a vacuum chamber by evacuating each unit to -28 inches of Hg. Qualified candidates were then installed in six atmospheres of SF6 and elevated to 100 kV at a nominal rate of 50 kV/sec. Twelve exposures to this level were made on each component while measuring the current. The screening criteria of this step was to ensure that no unit exhibited an abnormal amount of leakage current after being charged to 100 kV during a two-second stress level. Figures 3 and 4 show a representative charge voltage and current of a sample component, respectively. The selected value for C1 consisted of an array of two 40-nF/100 kV capacitors connected in series. As mentioned above, this provided a nominal design safety margin of two for these components.

**Figure 3** The above wave shape provides a typical view of the applied voltage to a 40-nF/100 kV oil filled capacitor.

**Figure 4** The above wave shape provides a typical view of the charge current to a capacitor during the screening process.

As indicated in Figure 4, the nominal leakage of the selected capacitors was 0.3 mA at 100 kV. As a result of this excessive leakage current, 25 watts of power was dissipated inside the capacitor. A process to reduce these internal losses has been developed by LLNL [5]. Ten capacitors of the current available units had a combined leakage of 3 mA. This indicates that a power supply minimum requirement for operation at 100 kV would have
an impedance of 33 MΩ and a minimum power requirement of 300 W. A 2 kW/125 kV Glassman power supply is used to support the impulser. The combined trigger circuit bias strings of 50-MΩ total gave additional power supply loading during the charge phase of operation. The equivalent loading to the power supply became nominally 20 MΩ, and at 100 kV consumes 500 W of power per shot. Added to the system-operating requirement of 500 J per shot, the maximum repetition rate of the circuit became 2 Hz for the given power supply. Open circuit measurements showed that the measured voltages and currents on the screened components were representative of the device under test (DUT) and not the test circuit. The measured current in the test fixture is 10,000 less than that measured across the DUT. As a result, all the current measured in the screening process was considered to be in the screened component.

A review of the selected capacitor information provided from Maxwell Laboratories, Inc. indicated that the units are rated for a 1 Hz operation. This specification is at full rated voltage and voltage reversal <20%. The resulting effects of a 2 Hz operation at 40% reduced operating voltage and minimal voltage reversal did not seem to be an issue. The slight reduction in the operating voltage does provide an estimated increase in the life expectancy by a factor of five. The anticipated life of 90% of the capacitors became 1.5x10⁵ discharges with these operating parameters. This is not adequate for a production-type application, but provides the needed information for the proof of concept.

**System Operation**

This section discusses the operation of the impulser. The intent of this robust design is to provide a reasonable tool to conduct experiments. As mentioned above, some of the selected components do not have the projected life expectancy desired for a final design but they will help establish the specific design requirements for the next generation.

The tuning of the impulse shaping components is done at air and reduced voltage to ensure that the diode does not close. The design impulse profile does not require the use of a high fidelity probe, a Tektronix P-6015A or equivalent will easily suffice. Figure 5 indicates the voltage measured across the diode. The impulse width is longer than required to illustrate the impulse quality.

**Conclusion**

High quality ion-injectors are possible when the design considerations include driver and load reflections caused by impedance mismatches. Simple circuits are easily fabricated to deliver required profiles. Limitations for long life high-current sources are main energy storage devices and switching gear. Hard-tube modulators represent the next level of injectors to achieve production like reliability and repetition rates.

**Reference**


