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A Hybrid Anode Reactor for the SLAC Modulator *

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Abstract

The SLAC modulators operate at 150 MW peak outputs at 120 pps with an average power of 87 kW. In an effort to improve modulator performance and reliability, we describe the design of a hybrid anode reactor using ferrite to decrease the ringing of the modulator output pulse, and incidentally reduce thyatron commutation loss. The design uses MnZn ferrite as a saturable lossy element to decrease the ringing in combination with NiZn ferrite as a saturable reactor for reducing the switching loss. The output ringing is product of the PFN stray capacitance and the leakage inductance of the pulse transformer, and if not suppressed causes premature failures of the output cable. The saturable switch aspect then offers the necessary rise time and pulse width recovery. While these two goals seem contrary, our initial performance objectives were met. Ringing on the output pulse is decreased by 50%. Switching loss reduction is measured by a thyatron temperature decrease of 15% as measured on the anode with a cathode reference temperature. The reactor packaging is very simple, and it is separated from the thyatron space so not to complicate thyatron replacement or modulator repairs and maintenance.

INTRODUCTION

The linac modulators were designed and built in the 60's, upgraded for the SLAC Linear Collider in the mid 80's, and despite their age but because of superb care, are still the backbone of a productive accelerator facility [1].

150 MW MODULATOR DATA		
Repetition Rate	120	Hz (maximum)
Thyatron Anode Voltage	46.7	kV
Thyatron Anode Current	6225	A
Pulse Transformer Ratio	1:15	
Voltage Pulse Width	5.0	μs (ESW)
Voltage Pulse Flattop	3.25	μs
Pulse Rise Time	0.8	μs
Pulse Fall Time	1.8	μs
Pulse Flattop Ripple	±0.25	%
Nominal PFN Impedance	4	Ω
Total PFN Capacitance	0.70	μF
Charging Inductance	2.4	H
PFN Charging Time	4.1	ms
Peak Charging Current	12.7	A
5045 KLYSTRON DATA		
Klystron Frequency	2656	MHz
Klystron Beam Voltage	350	kV
Klystron Beam Current	414	A
Microperveance	2	
Peak Power Out	67	MW
Peak Input Power	500	W
Power Gain	50	dB (minimum)
RF Pulse Width	3.5	μs

TABLE I 150 MW modulator and 67 MW klystron specifications.

Table I presents the modulator specifications and sets the parameters for the design of the anode reactor. The reactor must operate at 50 kV and handle 6225 A. The unit must not reduce the pulse flat top to less than 3.25 μs which implies minimum degradation of the present rise time.

I. REDUCING THE OUTPUT RINGING

Figure 1 is a waveform showing the oscillation at the modulator output as measured on the triaxial cable in the modulator. This is the cable that exits the cabinet to connect to the pulse transformer tank.

This voltage ringing is a result of the PFN stray capacitance discharging into the pulse transformer leakage inductance and a cable impedance to load mismatch ($Z_{\text{cable}} > Z_{\text{load}}$). In this circuit stray capacitance is impossible to eliminate, and while it would help, lowering the impedance of the cable is also a difficult process with only a minor effect.

Figure 1. The output pulse and oscillation.

The oscillation can ring up to 50 kV, essentially twice the output voltage, and severely stress the dielectric in the cable. Cable lifetime (MTBF) for a 120 Hz rep rate is approximately 14,000 hours. It typically requires an hour to replace a cable, thereby reducing modulator availability [2]. Cable failures also result in damage to other modulator components. Occasionally the cable socket on the pulse transformer tank is damaged which then requires klystron and tank replacement. The goal for cable lifetime is 30,000 hours so that a cable replacement is on a par with actual klystron lifetime.

Two circuit techniques were considered for reducing the output ringing: reactive turn-on control of the thyatron, and resistor-capacitor snubbing across the cable. Realizing the requirements presented for modulator performance and availability, the reactive technique was chosen as the most practical, reliable and compact. The snubber circuit would require a resistor and capacitor, two high voltage and low inductance components, consequent high voltage terminations, and isolation from conductive surfaces in a very restricted space within the cabinet.

Another benefit of the ferrite approach would be the possibility of compensating for the slower rise time through the introduction of a saturable element in the overall circuit. The saturating ferrite could retard the thyatron current transfer and decrease commutation losses, thereby increasing thyatron lifetime [3],[4]. This latter effect is of course moderated by the initial goal of controlling the output pulse ringing.

II. SELECTING THE FERRITE

Ferrite selection for overshoot reduction is not as straight forward as selecting ferrite for a saturable switching element. The ferrite should exhibit some definite hysteresis along with high permeability, and high B_{max} swing. The hysteresis provides the lossy reactive aspect for suppressing ringing while the high μ and B_{max} allow some control of thyatron turn-on.

CMD5005 was the first ferrite tested in the modulator. This is an excellent material for fast kicker magnets and is thoroughly characterized for high frequency applications. A supply of various sized toroids were available for early testing. But realizing that the CMD5005 is often used as a saturable switch, three other materials were acquired and investigated. All are listed with their respective manufacturers and test data in Table II. A test set was used to generate B-H plots for individual ferrites. Sets of specific ferrite

Figure 5. The delay between anode voltage fall and reactor voltage fall for the reactor set with 6-CMD5005's and 6-3C80's.

Figure 6. The delay between anode voltage fall and reactor voltage fall for the reactor set with 6-CMD10's and 6-3C80's.

The CMD10 ferrite exhibits twice the hysteresis loop of the CMD5005 with a significant increase in B_{max} , and this was the material that alone or in combination with the 3C80 resulted in major decrease in the ringing without a rise time or pulse width penalty.

Figure 6 shows the voltage fall waveforms of the CMD10 and 3C80 ferrite combination in the modulator.

Figure 7 presents output pulses for two reactor core combinations. An examination of the upper waveform in Figure 7 reveals that the peak ringing is reduced by 32 % as compared with the ringing in Figure 1 for the original modulator output.

Figure 7. Comparison of output pulses for two reactor versions.

The lower waveform of Figure 7 shows the reduction in ringing for the final design. This involved replacing two of the 3C80 cores with a pair of the larger MN80 cores. In an earlier experiment, up to six additional 3C80's were added to the circuit, but as the ringing decreased, so did the pulse width at the flattop. The final reactor core combination consists of six CMD10's, four 3C80's and two MN80's. This combination resulted in a 52% reduction in peak ringing, and the 5 MHz oscillation is damped out in eight cycles as compared to the 13 cycles in Figure 1.

When the load current pulses are compared for the "no reactor" case and the two combinations above, it is as expected with the final reactor combination producing the narrowest pulse flattop, but still within the 3.25 μs minimum that is required. With the reactor installed, the output rise time is reactor dependent, but the pulse flattop tilt and width are adjustable. The range of adjustment is a function of the excess energy in the PFN that is normally available for positive mis-match [5].

Figure 8 shows three output current waveforms with the "no reactor" and two reactor situations. There is only a minor 40 ns rise time slowdown with the CMD10, 3C80 and MN80 combination.

Figure 8. Comparison of output current pulses

During one phase of the design process, six MN80's were combined with six 3C80's for a 70 % reduction of output ringing, but a very narrow pulse flattop resulted that required the total PFN energy, leaving none for mis-match adjustment.

Figure 9. Comparison of time delay for anode current rise.

III. IMPROVING THYRATRON COMMUTATION

When the voltage fall time waveforms are compared in Figures 4, 5 and 6, it is obvious that the commutation losses are increasing as more lossy ferrite is added to control the ringing. However,

Figure 6, shows that the anode fall time occurs 100 ns before the thyatron begins to conduct full current. And even when the initial current is considered as Figure 9 illustrates, the time delay between the zero anode voltage and thyatron conduction is 50 ns. The reactor must be lowering the switching loss of the thyatron.

The effect can measured and the loss difference calculated, if the discharge circuit is completely coaxial and the appropriate instrumentation exists for accurately obtaining the anode voltage fall and anode current rise [6].

Lacking the any semblance of coaxial geometry or accurate instrumentation, the anode surface temperatures were measured with and without the reactor using the thyatron deck adjacent to the cathode flange as a temperature reference and the incoming air temperature as a secondary reference.

The summary of the measurements given in Table III indicates a distinct decrease in the thyatron temperature. The mean surface temperature over the 11 sq. in. central area of the anode dropped by 3 °C. The anode hot spot temperature decreased 5 °C. This temperature reduction may increase thyatron lifetime, but it is of such modest proportions that it could take several years for the effect to be statistically noticed.

Anode Temperature Minus Reference	Surface Area sq. in.	Without Reactor °C	With Reactor °C	Temp Dec ΔT °C
$T_{\text{anode}} - T_{\text{deck}}$	11	20.3	17.3	3
$T_{\text{anode}} - T_{\text{ambient}}$	11	29.6	27.1	2.5
$T_{\text{anode}} - T_{\text{deck}}$	1	29.3	24.1	5.2
$T_{\text{anode}} - T_{\text{ambient}}$	1	38.7	34.1	4.6

TABLE III. The reactor lowers the thyatron temperature.

IV. REACTOR CONSTRUCTION

The SLAC linac uses 244 modulators. Because of this number, any modulator improvement demands economical construction, packaging and installation. Especially considering the cost of the ferrite, even if balanced by the potential savings in cables and thyatrons.

Figure 10 illustrates the reactor assembly minus the toroid retainer. The four 3C80 are C-section ferrites that are fastened together when attached to the channel. The single reactor turn uses six parallel silicone insulated wires. The overall length of the fiberglass channel is 24 inches. The dimension determined by the width of the PFN cabinet. The toroids are spaced an inch apart to accommodate core and wire cooling.

The assembly is mounted on the PFN front panel. When the modulators were upgraded, the 20 section PFN was replaced with a 16 section circuit. The two top and bottom PFN locations were left empty. Prior to mounting the reactor, two middle PFN sections are removed and re-located to the two bottom positions. The reactor assembly then fills the space left in the middle of the PFN cabinet. The reactor is completely separated from the thyatron except for a short length a cable. This isolation permits rapid thyatron replacement without compromising repair times.

Figures 11 and 12 show the actual reactor installation in the modulator.

Figure 10. The ferrite line-up for the anode reactor.

V. ACKNOWLEDGMENTS

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VI. REFERENCES

- [1] A.R. Donaldson *et al.*, "The Second Generation SLAC Modulator," 1986 17 th Power Modulator Symposium, IEEE Conference Record CH2262-4/86/0000-230.
- [2] A.R. Donaldson and J. R. Ashton, "SLAC Modulator Operation and Reliability in the SLC Era," 1986 20 th Power Modulator Symposium, IEEE Conference Record CH3180-7/92/0000-152.
- [3] L. Ducimetiere and D.C. Fiander, "Commutation Losses of a Multigap High Voltage Thyatron," 1990 19 th Power Modulator Symposium, IEEE Conference Record CH2839-9/90/0000-0248.
- [4] K. Rust and G. McDuff, "Life Extension of Thyatrons in Short Pulse Circuits," 1990 19 th Power Modulator Symposium, IEEE Conference Record CH2839-9/90/0000-0290.
- [5] A.R. Donaldson and J. R. Ashton, *ibid.*
- [6] L. Ducimetiere and D.C. Fiander, *ibid.*

Figure 11. The reactor mounting location on the PFN cabinet.

Figure 12. The thyatron, PFN and reactor in the cabinet.