Accelerator Power Concepts
Using Isolated Transmission Lines
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USING ISOLATED TRANSMISSION LINES
by
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ABSTRACT

This report outlines the rationale and the advantages of using multiple transmission-line sections isolated by transit time and inductance in accelerating high-current (≥10-kA), short-pulse (≤100-ns) particle beams to the multimegavolt level. The main advantages of this system include reducing the number of output switches required per output pulse by nearly an order of magnitude over conventional systems and increasing the system capability for repetition-rate operation. The isolated transmission-line concept is developed, and possible modes of operation are outlined. In addition, a point design of a 10-kA short-pulse accelerator is presented.

I. INTRODUCTION

The accelerator power schemes currently used in directed energy systems such as the experimental test accelerator (ETA) and the advanced test accelerator (ATA) require many high-quality (high-voltage, low-inductance, low-jitter, and high-repetition rate) switches to accelerate single pulses in sequence. These switches must be high quality because they play a primary role in the acceleration scheme.

The isolated transmission-line accelerator (ITLA) power scheme presented here uses high-power switches in a secondary role that requires lower quality units. ETA/ATA-type acceleration power schemes use the switching action of one switch only one time, whereas the ITLA scheme uses one switch many times through transit-time isolation. The ITLA power scheme can reduce by 10 times the number of switches required by an ETA/ATA system or can provide acceleration power for up to 10 simultaneous ETA/ATA-type pulses with the same number of switches. Another advantage of the ITLA power scheme is that because the system is not limited by ferrite size as is the ETA/ATA system, it can use the higher saturation flux density and slower frequency response of tape-wound iron cores.

II. ETA/ATA ACCELERATOR POWER SYSTEM

The ETA/ATA accelerator power system uses many pulse generator units that are transformer-coupled sequentially to the pulse being accelerated. A simplified schematic of a one-pulse generator system, which consists of a Blumlein line, a large ferrite toroid, and a high-quality switch, is shown in Fig. 1. The Blumlein line initially is charged to voltage $V_0$. The main switch is closed at time $t = 0$ to start the propagation of a shorting wave toward the load from A to B. One-line transit time $T_L$ later, the wave appears at B. If the beam pulse leading edge is at C, the beam pulse duration is $T_p = 2T_L$, and the beam pulse amplitude is $I_p = V_0/2I_L$. The Blumlein line wave experiences a matched load because of the transformer-coupled beam current. The beam pulse is accelerated through a potential of $V_0$ for a time equal to $2T_L = T_p$. This method of system operation places severe requirements on the operation of the main switch.
First, the switch must be triggered with low jitter so the Blumlein line wave arrival at B is synchronized with the beam pulse arrival. Second, the switch must close in a time \( T_c \), much less than the one-way line transit time \( T_L \). The switch closure time \( T_c \) is determined by the switch inductance \( L_s \) and the line impedance \( Z_L \), or \( T_c \approx 3L_s/Z_L \ll T_L/10 \). Thus, the switch inductance must be low to permit fast-current risetimes in low-impedance circuits. In addition, the switch also must have a high repetition-rate capability because the switch must be triggered one time for each pulse to be accelerated.

### III. ITLA Power System

The ITLA power concept will be developed with a progression of circuits and applications. A basic charged transmission-line accelerator is shown in Fig. 2. This transmission line with impedance \( Z_L \) is charged to voltage \( V_0 \) and will accelerate a matched beam current pulse of \( I_0 = V_0/2Z \) and pulse length of \( T_p = 2T_L \) to an additional potential of \( V_0/2 \) (assuming the beam initially is relativistic). This system can be expanded so a similar beam pulse with twice the peak current is accelerated by \( V_0/2 \) by two identical parallel strip lines, as shown in Fig. 3. The two transmission-line accelerator schemes in Figs. 2 and 3 do not require a switch to control the interaction of the transmission line and beam pulse. However, the change in beam potential is only one-half of the charging potential for these matched systems.

![Fig. 1. ETA/ATA-type induction accelerator module.](image1)

![Fig. 2. Charged transmission-line accelerator.](image2)

![Fig. 3. Parallel charged transmission-line accelerator.](image3)
Operation of a Blumlein line can be characterized as voltage-vector inversion. The sum of the voltage vectors from A to C in the generalized Blumlein line of Fig. 5a is zero before the switch is closed at time \( t = 0 \). If the Blumlein line is not loaded across A and C and the switch is closed at \( t = 0 \), a voltage shorting wave propagates down the transmission line to the open circuit at terminals AB and inverts the voltage vector in the bottom transmission line of Fig. 5a. When the transient wave returns to the switch at \( t = 2T_L \), the voltage vector in the bottom line is inverted everywhere as shown in Fig. 5b. At time \( t = 2T_L \), the voltage potential available across A and C for the entire line is \( 2V_0 \). If an accelerator structure were connected to A and C, the inversion switch were opened, and a matched beam pulse were injected \((I_p = V_0/4Z, T_p = 2T_L)\) at \( t = 2T_L \), the pulse would be accelerated through a potential of \( V_0 \) for a period of \( 2T_L \) until \( t = 3T_L \). Note that the perfect switch was opened in this example to make the accelerator operation compatible with transmission-line operation.

This example is used only to illustrate the principle of voltage-vector inversion and its use in an accelerator system.

If the transmission lines of Fig. 5 are much longer than the pulse to be accelerated and, in particular, an integral number of pulse lengths, \( T_L = mT_p \), several pulses can be accelerated in parallel by the same set of transmission lines as shown in Fig. 6, where \( m = 3 \). Note that this arrangement consists of three dual Blumlein line accelerators (previously illustrated in Fig. 4) connected in series, but with only one switch used for voltage inversion.

Operation of the system in Fig. 6 can be made consistent with transmission-line operation (no switch opening required) in the following manner. The two transmission lines are charged initially to voltage \( V_0 \). The switch is closed at \( t = 0 \) and the voltage inversion takes place as noted for the system in Fig. 5. The accelerator structures in Fig. 6 must be designed to cause minimal discontinuities and, therefore, reflections of the inversion transients. Also, the accelerator structures must not be loaded (no beam pulse present) during the inversion process. After the inversion wave has reached the open end and returned to A \((t = 5.5 T_p)\), matched beam pulses \((I_p = V_0/4Z, T_p = T_L/3)\) are simultaneously injected at A, B, and C. These pulses are accelerated by a potential \( V_0 \) for a period of \( T_p = T_L/3 \). The individual accelerator structures at A, B, and C are isolated from each other by the transit time of the transmission line.

This particular arrangement offers several advantages. First, only one switch is used to accelerate three beam pulses. Second, the switch used only to invert the long transmission-line potential vectors does not have to be as high quality as a switch that is involved directly in coupling the accelerator to the beam. A third advantage, which is not immediately obvious, is the repetition-rate advantages of this pulse system. The bottom transmission-line section in Fig. 6 from input A to the switch can be lengthened and a transmission-line fan-out can be added to multiplex several switches, one in each leg of the fan-out. This arrangement permits higher rate operation through easy switch multiplexing. The short-duration transients caused by
Fig. 6. Transit-time ITLA.

the impedance discontinuities of the transmission-line fan-out will not affect the accelerator structures and process because of the time delay between switch closure and acceleration.

Another mode of operation is indicated in Fig. 6 by the dashed lines. If the transit time of an accelerated particle from the output of accelerator section A to the input of section B, \( T_{AB} \), could be made much less than the transmission-line transit time between structures of \( T_p \), a single beam could, in principle, be accelerated \( m \) times (\( m = 3 \)) by the same structure, and the beam trajectory generally would be helical through the structure.

Because of the practical difficulties of providing the helical beam trajectory and the impossibility of reducing \( T_{AB} \ll T_p \) for this system, an alternative is to deform the transmission lines into a helix and provide a straight beam trajectory. This method also makes the condition \( T_{AB} \ll T_p \) feasible, which is the essence of the helical edge-wound strip-line accelerator (HEWSLA) concept.²

A major practical consideration in accelerator systems involves enclosed potentials or grounded beam input and output ports. Another modification of the accelerator system in Fig. 6, shown in Fig. 7, provides grounded beam input and output ports. Basically, inductive isolation separates the bottom line from the top line, and the opposite polarity acceleration potential is applied across this inductive isolation. The beam trajectory passes through a closed metal cylinder or drift tube and does not experience the external decelerating field. The isolation inductance \( L_z \) of this path is made large so that the discharge time of the transmission-line capacitance \( C_L \) is longer than the inversion time \( T_i \), or \( \sqrt{L_z C_L} \approx \frac{1}{T_i} \).

The inductance is enlarged by using tape-wound ferromagnetic cores. In the electrical circuit of this scheme, the isolating inductance appears in parallel with the beam pulse load during acceleration. Thus, this scheme can be labeled an induction accelerator. However, this concept can use lower frequency response and higher saturation flux materials (steel instead of ferrite) than the ETA/ATA-type system can.

The inductive isolation can be combined with transit time isolation to accelerate \( m \) pulses simultaneously (parallel) or one pulse \( m \) times (series). A series ITLA module with \( m = 10 \) is shown in Fig. 6, and a parallel ITLA in which three pulses are accelerated simultaneously is shown in Fig. 9. The parallel module could be used in an accelerator system that would series the beam pulses after acceleration with beam-handling techniques. In this context, the parallel ITLA can be considered a beam chopper.

Fig. 7. Transit-time and inductance ITLA.

Fig. 8. Helical ITLA.
where $d$ is the line separation, $W$ is the transmission-line width, $\mu_r$ is the effective permeability of the core, $\mu_0$ is the permeability of free space, $\varepsilon_0$ is the permittivity of free space, and $\varepsilon_r$ is the dielectric relative permittivity.

The line separation is determined by the dielectric strength $K_s$ of the medium between the conductors or

$$d \geq \frac{V_0}{K_s}.\quad (3)$$

From Eqs. (2) and (3) the transmission line width is given by

$$W \geq \frac{I_p}{K_s} \left(\frac{\mu_0 \mu_r}{\varepsilon_0 \varepsilon_r}\right)^{1/2}.\quad (4)$$

To minimize the difference in the transmission-line transit time between the inside and the outside radii, the major helix radius $R_h$ is chosen such that

$$R_h \geq 10 \, W.\quad (5)$$

In addition, the number of pulses $m$ to be accelerated and the helix radius $R_h$ are iterated to obtain an integer value for $m$ in the equation relating the turn transit time $T_L$ to the beam pulse width $T_p$, or

$$T_L = m T_p = \frac{2 \pi R_h \varepsilon_r^{1/2}}{C}.\quad (6)$$

where $C$ is the speed of light in vacuum.

Next, the erection or total line-inversion time $T_i$ is limited by the loss characteristics of the system. The first constraint on $T_i$ and thus on the number of helix turns is the resistive decay time $T_{RC}$ or
The line capacitance per unit length is

\[ C_L = \frac{\varepsilon_0 \varepsilon_r W}{d} \]  

and the line shunt resistance per unit length is

\[ R_L = \frac{\rho d}{W} \]  

such that

\[ T_{rc} = R_L C_L = \rho_0 \varepsilon_r \]  

The inductance of one turn is

\[ L_T = \mu_0 \left( \frac{\mu_r A_c}{L_c} + \frac{A_S}{L_S} \right) \]  

where \( A_c \) is the core area, \( L_c \) is the core path length, and \( \mu_r \) is the effective permeability of the core. The term \( A_S/L_S \) represents the stray inductance and \( n \) is the number of turns through the core. For the series system \( n = 1 \) and \( \mu_r A_c/L_c \gg A_S/L_S \),

\[ L_T \approx \frac{\mu_0 \mu_r A_c}{L_c} \]  

where \( \mu_0 \) is the permeability of free space and \( \mu_r \) is the relative permeability of the core.

The minimum core area must be such that

\[ 2A_c B_s = 2V_0 T_i \]  

where \( B_s \) is the magnetic material saturation flux density or

\[ A_c = \frac{V_0 T_i}{B_s} \]  

To minimize inductive loading, the product of core area and relative permittivity, \( \mu_r A_c \), is chosen to permit the number of helical turns desired. The turn inductance through the core is now

\[ L_T = \frac{\mu_0 \mu_r v_o T_i}{L_c B_s} \]  

where \( \alpha \) is the increase in the \( \mu_r A_c \) product required to obtain the desired number of helical turns \( n \). The magnetic length of the core can be approximated by

\[ l_c = \frac{(OD + ID)}{2} \]  

where OD is the core outside diameter and ID is the core inside diameter. The LC resonant time is finally

\[ T_{LC} = \pi (L_T C_T)^{1/2} = \left[ \frac{4 \pi \mu_0 \mu_r V_0 R_h \varepsilon_r \varepsilon_f W \alpha}{(OD + ID) B_s d} \right]^{1/2} \]  

where \( C_T \) is the capacitance of one helical turn.

The inversion time \( T_i \) is related to the number of helical turns and to the structure parameters by

\[ T_i = 2n T_L = \frac{4n \pi R_h \varepsilon_r 1/2}{C} \]  

The inversion time must be such that

\[ T_i \leq 0.1 T_{rc} \]  

or

\[ 2A_c B_s = 2V_0 T_i \]  

where \( B_s \) is the magnetic material saturation flux density or
TABLE I
PARAMETERS FOR OIL-INSULATED SERIES ITLA MODULE

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line-charge voltage (V)</td>
<td>2.5 E+05</td>
</tr>
<tr>
<td>Beam current (A)</td>
<td>1.0 E+04</td>
</tr>
<tr>
<td>Beam pulse width (s)</td>
<td>5.0 E-08</td>
</tr>
<tr>
<td>Line separation (m)</td>
<td>3.1 E-02</td>
</tr>
<tr>
<td>Line width (m)</td>
<td>3.1 E-01</td>
</tr>
<tr>
<td>Helix radius (m)</td>
<td>3.1 E-00</td>
</tr>
<tr>
<td>Line impedance (Ω)</td>
<td>2.5 E-01</td>
</tr>
<tr>
<td>Dielectric strength (V/m)</td>
<td>0.0 E+00</td>
</tr>
<tr>
<td>Relative dielectric constant</td>
<td>2.3 E+06</td>
</tr>
<tr>
<td>Number of parallel pulses</td>
<td>2</td>
</tr>
<tr>
<td>Multiple of min flux</td>
<td>1.2 E+00</td>
</tr>
<tr>
<td>Erection time (s)</td>
<td>1.0 E-06</td>
</tr>
<tr>
<td>Resistive decay time (s)</td>
<td>2.0 E-01</td>
</tr>
<tr>
<td>Core area (m²)</td>
<td>2.0 E-01</td>
</tr>
<tr>
<td>Core width along beam (m)</td>
<td>2.9 E-01</td>
</tr>
<tr>
<td>RC limit of number of turns</td>
<td>1.0 E+05</td>
</tr>
<tr>
<td>LC limit of number of turns</td>
<td>5.0 E+00</td>
</tr>
<tr>
<td>Maximum number of turns</td>
<td>5.0 E+00</td>
</tr>
<tr>
<td>Acceleration per module (V)</td>
<td>2.5 E+06</td>
</tr>
</tbody>
</table>

L26, and L28 represent the beam inductance and associated resistance. (NET-2 requires that each inductor have a specified resistance.) The computer-generated wave form in Fig. 12 illustrates the erection of the three-turn ITLA. The acceleration potential in Fig. 12 is the sum of the voltages across the L26, and L28.

If a reasonable value of n, the number of helical turns and half the number of switches to be replaced is chosen, the value of α and the erection time can be determined.

Thus, all the parameters for a series ITLA structure can be determined. The values for an oil-insulated, 10-kA accelerator are listed in Table I.

V. CIRCUIT SIMULATION

The operation of a three-turn ITLA was simulated using the NET-2 circuit analysis program. The circuit model is shown in Fig. 11, where the rectangular boxes represent transmission-line sections, V1 is the 250-kV charging supply, and SI is the erection switch. Inductors L19, L20, and L21 represent the turn-isolation inductance associated with the steel cores. Inductors L7, L9, L11, L24, L26, and L28 represent the beam inductance and associated resistance. (NET-2 requires that each inductor have a specified resistance.) The computer-generated wave form in Fig. 12 illustrates the erection of the three-turn ITLA. The acceleration potential in Fig. 12 is the sum of the voltages across the

Fig. 11. Three-turn series ITLA circuit model.
transmission line at each acceleration port. Initially, the sum of the charge voltages equals zero. After the main switch (S1) is closed, the wave transients passing by the beam switches invert the bottom line so that the peak erection voltage is 1500 kV or \( \frac{1}{m} \text{ charge voltage of 250 kV} \). If the beam switches are closed at peak voltage \( t = 600 \text{ ns} \), a majority of the transmission-line energy is delivered to the load resistors (associated with load inductances). The effective current through one beam load section is shown in Fig. 13, and the acceleration potential imparted to the beam is shown in Fig. 14. Note that the high-frequency spikes in the circuit simulation are due to the ideal transmission-line sections. The circuit simulation can be considered a first-order approximation. For example, all the beam switches were closed simultaneously, but in reality they should be staggered to represent the beam pulse traveling through the accelerator structures in series.

VI. CONCLUSIONS

Isolated transmission lines can be used to reduce the number, quality, and requirements of high-power switches in pulsed accelerators. The parameters in Table I indicate that the number of switches in an ETA/ATA-type system can be reduced 10 times for a symmetrical series ITLA in which the erection switch is in the center of a 10-turn helix. Operation of the switches in a repetition mode is...
facilitated in the ITLA system because timing and switching requirements are reduced. Switch multiplexing also is facilitated by the use of switches in a secondary rather than a primary role.

REFERENCES

